JC564 U.S. PTO
09/538670
03/30/00

		ISSUE CLASSIFICATION
712	2C	Subclass
712		Class

OS-0443

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/538670		712	21	2782 2754	Donghie

APPLICANTS
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TITLE
Method and apparatus for allocating functional units in a multithreaded vliw processor

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
712	24	718	103	102				
INTERNATIONAL CLASSIFICATION								
G06F	1T/00							
G06F	9/46							
				☐ Continued on Issue Slip Inside File Jacket				

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	5	9	7B	16	1
☐ The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner)		(Date)	1/8/05	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	LARRY D. DONAGHUE PRIMARY EXAMINER				
				ISSUE FEE	
_____				Amount Due	Date Paid
	(Primary Examiner)		05/31/05 (Date)	1400.00	
☐ The terminal ___ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner)		(Date)		

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